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☑ **document 61 of 306** Order Document

INSPEC - 1969 to date (INZZ)

Accession number & update
6296104, B1999-08-1265B-054, C1999-08-5210B-037; 19990701.

Title
Equivalence checking of combinational circuits using Boolean expression diagrams.

Author(s)
Hulgaard-H; Williams-P-F; Andersen-H-R.

Author affiliation
Dept of Inf Technol, Tech Univ, Lyngby, Denmark.

Source
IEEE-Transactions-on-Computer-Aided-Design-of-Integrated-Circuits-and-Systems (USA), vol.18, no.7, p.903-17, July 1999. , Published: IEEE.

CODEN
ITCSDI.

ISSN
ISSN: 0278-0070, CCCC: 0278-0070/99/ ($10.00).

Availability
SICI: 0278-0070(199907)18:7L.903:ECCC; 1-N
Electronic Journal Document Number: S0278-0070(99)05031-9.

Publication year
1999.

Language
EN.

Publication type
J Journal Paper.

Treatment codes
P Practical; T Theoretical or Mathematical; X Experimental.

Abstract
The combinational logic-level equivalence problem is to determine whether two given combinational circuits implement the same Boolean function. This problem arises in a number of computer-aided design (CAD) applications, for example when checking the correctness of incremental design changes (performed either manually or by a design automation tool). This paper introduces a data structure called Boolean expression diagrams (BEDs) and two algorithms for transforming a BED into a reduced ordered binary decision diagram (OBDD). BEDs are capable of representing any Boolean circuit in linear

space and can exploit structural similarities between the two circuits that are compared. These properties make BEDs suitable for verifying the equivalence of combinational circuits. BEDs can be seen as an intermediate representation between circuits (which are compact) and OBDD's (which are canonical). Based on a large number of combinational circuits, we demonstrate that BEDs either outperform or achieve results comparable to both standard OBDD approaches and the techniques specifically developed to exploit structural similarities for efficiently solving the equivalence problem. Due to the simplicity and generality of BEDs, it is to be expected that combining them with other approaches to equivalence checking will be both straightforward and beneficial. (46 refs).

Descriptors

binary-decision-diagrams; Boolean-functions; circuit-CAD; combinational-circuits; data-structures; logic-CAD.

Keywords

equivalence checking; combinational circuits; Boolean expression diagrams; CAD applications; incremental design changes; design automation tool; data structure; BED; reduced ordered binary decision diagram; linear space; structural similarities.

Classification codes

B1265B (Logic circuits).
B1265A (Digital circuit design, modelling and testing).
B1130B (Computer-aided circuit analysis and design).
B0250 (Combinatorial mathematics).
C5210B (Computer-aided logic design).
C4230B (Combinatorial switching theory).
C5120 (Logic and switching circuits).
C7410D (Electronic engineering computing).
C1160 (Combinatorial mathematics).

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☑ **document 46 of 306** Order Document

INSPEC - 1969 to date (INZZ)

Accession number & update
6615178, C2000-07-4210-046; 20000601.

Title
Difference decision diagrams.

Author(s)
Moller-J; Lichtenberg-J; Andersen-H-R; Hulgaard-H; Ed. by Flum-J; Rodriguez-Artalejo-M.

Author affiliation
IT Univ in Copenhagen, Denmark.

Source
Proceedings of CSL'99: Annual Conference of the European Association for Computer Science Logic, Madrid, Spain, 20-25 Sept. 1999.
Sponsors: Comision Interministerial de Ciencia y Tecnologia, Dept. Arquitectura de Computadores y Automatica, et al.
In: p.111-25, 1999.

ISSN
ISBN: 3-540-66536-6.

Publication year
1999.

Language
EN.

Publication type
CPP Conference Paper.

Treatment codes
T Theoretical or Mathematical; X Experimental.

Abstract
This paper describes a new data structure, difference decision diagrams (DDD), for representing a Boolean logic over inequalities of the form x-y<or=c where the variables are integer or real-valued. We give algorithms for manipulating DDD and for determining validity, satisfiability, and equivalence. DDD enable an efficient verification of timed systems modeled as, for example, timed automata or timed Petri nets, since both the states and their associated timing information are represented symbolically, similar to how BDD represent Boolean predicates. We demonstrate the efficiency of DDD by analyzing a timed system and compare the results with the tools KRONOS and UPPAAL. (28 refs).

Descriptors
automata-theory; Boolean-functions; computability; data-structures; decision-diagrams; formal-verification; Petri-nets; real-time-systems.

Keywords

difference decision diagrams; data structure; Boolean logic; integer variables; real valued variables; validity; satisfiability; equivalence; timed systems; timed automata; timed Petri nets; KRONOS; UPPAAL.

Classification codes

C4210 (Formal logic).
C1160 (Combinatorial mathematics).
C6110F (Formal methods).
C6120 (File organisation).

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☑ **document 82 of 306** Order Document

INSPEC - 1969 to date (INZZ)

Accession number & update
5658864, C9709-4210-093; 970805.

Title
Boolean expression diagrams.

Author(s)
Andersen-H-R; Hulgaard-H.

Author affiliation
Dept of Inf Technol, Tech Univ, Lyngby, Denmark.

Source
Proceedings of Twelfth Annual IEEE Symposium on Logic in Computer Science, Warsaw, Poland, 29 June-2 July 1997.
Sponsors: IEEE Tech. Committee on Math. Found. Comput., IEEE Comput. Soc., SIGACT ACM, Assoc. Symbolic LogicEur. Assoc. Theoretical Comput. SciBRICS, Univ. AarhusInst. Inf., Warsaw UnivPolish Acad. Sci.: Math. Inst. & Committee for MathPolish-Japanese Inst. Comput. TechPolish Minist. EducSun Microsyst., PolandUS Office of Naval ResWroclaw Univ.
In: p.88-98, 1997.

ISSN
ISBN: 0-8186-7925-5, CCCC: 1043-6871/97/ ($10.00).

Publication year
1997.

Language
EN.

Publication type
CPP Conference Paper.

Treatment codes
T Theoretical or Mathematical.

Abstract
This paper presents a new data structure called Boolean Expression Diagrams (BEDs) for representing and manipulating Boolean functions. BEDs are a generalization of Binary Decision Diagrams (BDDs) which can represent any Boolean circuit in linear space and still maintain many of the desirable properties of BDDs. Two algorithms are described for transforming a BED into a reduced ordered BDD. One closely mimics the BDD apply-operator while the other can exploit the structural information of the Boolean expression. The efficacy of the BED representation is demonstrated by verifying that the redundant and non-redundant versions of the ISCAS 85 benchmark circuits are identical. In particular, it is verified that the two 16-bit multiplication circuits (c6288 and c6288nr) implement the same Boolean functions. Using BEDs, this verification problem is solved in less than a second, while using

standard BDD techniques this problem is infeasible. BEDs are useful in applications where the end-result as a reduced ordered BDD is small, for example for tautology checking. (23 refs).

Descriptors

Boolean-functions; data-structures; decision-tables.

Keywords

data structure; Boolean Expression Diagrams; Boolean functions; Binary Decision Diagrams; reduced ordered; tautology checking.

Classification codes

C4210 (Formal logic).
C4230 (Switching theory).

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Inventor Information for 09/996745

Inventor Name	City	State/Country
LICHTENBERG, JAKOB	CHARLOTTENLUND	DENMARK
ANDERSEN, HENRIK REIF	BAGSVAERD	DENMARK
HULGAARD, HENRIK	COPENHAGEN	DENMARK
MOLLER, JESPER	COPENHAGEN	DENMARK
RASMUSSEN, ANDERS STEEN	LYNGBY	DENMARK

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Inventor Name Search Result

Your Search was:

Last Name = RASMUSSEN
First Name = ANDERS

Application#	Patent#	Status	Date Filed	Title	Inventor Name 2
60251862	Not Issued	159	12/08/2000	VIRTUAL TABULATION	RASMUSSEN, ANDERS STEEN
09996745	Not Issued	030	11/30/2001	METHOD OF CONFIGURING A PRODUCT	RASMUSSEN, ANDERS STEEN

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = MOLLER
First Name = JESPER

Application#	Patent#	Status	Date Filed	Title	Inventor Name 2
09996745	Not Issued	030	11/30/2001	METHOD OF CONFIGURING A PRODUCT	MOLLER, JESPER
09763753	Not Issued	041	04/27/2001	DATA STRUCTURE AND ITS USE	MOLLER, JESPER

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = HULGAARD
First Name = HENRIK

Application#	Patent#	Status	Date Filed	Title	Inventor Name 4
60251862	Not Issued	159	12/08/2000	VIRTUAL TABULATION	HULGAARD, HENRIK
09996745	Not Issued	030	11/30/2001	METHOD OF CONFIGURING A PRODUCT	HULGAARD, HENRIK
09763753	Not Issued	041	04/27/2001	DATA STRUCTURE AND ITS USE	HULGAARD, HENRIK
09116835	6408262	150	07/17/1998	METHOD AND AN APPARATUS FOR ANALYZING A STATE BASED SYSTEM MODEL	HULGAARD, HENRIK

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = ANDERSEN
First Name = HENRIK

Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
60424339	Not Issued	159	11/07/2002	INCHWORM MECHANISM	ANDERSEN, HENRIK
60331018	Not Issued	159	11/06/2001	INCHWORM MECHANISM	ANDERSEN, HENRIK
60301837	Not Issued	159	07/02/2001	FLEXIBLE TOOL FOR HANDLING SMALL OBJECTS	ANDERSEN, HENRIK
60251862	Not Issued	159	12/08/2000	VIRTUAL TABULATION	ANDERSEN, HENRIK REIF
60200029	Not Issued	159	04/26/2000	ANKLE-FOOT ORTHOSIS AND A METHOD FOR MAKING THE SAME	ANDERSEN, HENRIK S.
60148564	Not Issued	159	08/13/1999	A CONVEYOR AND A METHOD FOR CONVEYING ARTICLES ALONG A CONVEYOR	ANDERSEN, HENRIK
60119036	Not Issued	159	02/08/1999	ENHANCEMENT OF BEVERAGE SHELF-LIFE WITH D-TAGATOSE	ANDERSEN, HENRIK
60081926	Not Issued	159	04/16/1998	PROCESS AND UNIT FOR THE COMBINED PRODUCTION OF AMMONIA SYNTHESIS GAS AND POWER	ANDERSEN, HENRIK S.
60075694	Not Issued	159	02/24/1998	PREBIOTIC COMPOSITION	ANDERSEN, HENRIK
60070480	Not Issued	159	01/05/1998	COMBINATION OF D-TAGATOSE AND A SWEETENER	ANDERSEN, HENRIK
60054858	Not Issued	159	08/06/1997	TILTABLE NUT	ANDERSEN, HENRIK
60050993	Not Issued	159	06/18/1997	DOSE SETTING DEVICE	ANDERSEN, HENRIK
10831220	Not Issued	030	04/26/2004	METHOD OF FLEXIBLE PROCESS PLANNING FOR	ANDERSEN, HENRIK JOHN

				SURFACE PROCESSES	
10511462	Not Issued	020	04/11/2005	METHOD FOR MEASURING CURRENTS IN A MOTOR CONTROLLER AND MOTOR CONTROLLER USING SUCH METHOD	ANDERSEN, HENRIK ROSENDAL
10498541	Not Issued	020	03/18/2005	NETWORK CONTROLLED SORTER CONVEYOR	ANDERSEN, HENRIK
10472213	Not Issued	030	10/15/2003	APPARATUS FOR FLUSHING PERIPHERAL ORGANS IN HUMANS OR ANIMALS	ANDERSEN, HENRIK
10462753	Not Issued	061	06/17/2003	CONVEYOR/SORTER SYSTEM, A LOADING CONVEYOR AND A CONTROL SYSTEM FOR SUCH CONVEYORS	ANDERSEN, HENRIK
10183559	6741912	150	06/28/2002	FLEXIBLE TOOL FOR HANDLING SMALL OBJECTS	ANDERSEN, HENRIK
09996745	Not Issued	030	11/30/2001	METHOD OF CONFIGURING A PRODUCT	ANDERSEN, HENRIK REIF
09870700	Not Issued	161	06/01/2001	CONVEYOR/SORTER SYSTEM, A LOADING CONVEYOR AND A CONTROL SYSTEM FOR SUCH CONVEYORS	ANDERSEN, HENRIK
09763753	Not Issued	041	04/27/2001	DATA STRUCTURE AND ITS USE	ANDERSEN, HENRIK REIF
09719769	6467532	150	12/18/2000	METHOD AND APPARATUS FOR COUNTER-GRAVITY CASTING OF METAL	ANDERSEN, HENRIK KLARSKOV
09255655	Not Issued	128	02/23/1999	USE OF D-TAGATOSE AS A PREBIOTIC FOOD COMPONENT	ANDERSEN, HENRIK
09227084	6432464	150	01/05/1999	SYNERGISTIC COMBINATION OF SWEETENERS INCLUDING D-TAGATOSE	ANDERSEN, HENRIK
09116835	6408262	150	07/17/1998	METHOD AND AN APPARATUS FOR ANALYZING A STATE BASED SYSTEM MODEL	ANDERSEN, HENRIK REIF
09094311	5961496	150	06/09/1998	TILTABLE NUT	ANDERSEN, HENRIK
09090144	6796970	150	06/04/1998	DOSE SETTING DEVICE	ANDERSEN, HENRIK
08913401	5980956	250	09/17/1997	DEOXYGENATION OF AN OIL	ANDERSEN,

				PRODUCT WITH A LACCASE	HENRIK
08863751	5750518	150	05/27/1997	NOVEL HETEROCYCLIC COMPOUNDS	ANDERSEN, HENRIK S.
08863749	5747481	150	05/27/1997	NOVEL HETEROCYCLIC COMPOUNDS	ANDERSEN, HENRIK S.
08863746	5846968	150	05/27/1997	N-SUBSTITUTED AZAHETEROCYCLIC CARBOXYLIC ACIDS AND ESTERS	ANDERSEN, HENRIK S.
08855781	6127343	250	05/12/1997	SOMATOSTATIN AGONISTS AND ANTAGONISTS	ANDERSEN, HENRIK S.
08638086	5773943	250	04/25/1996	DRIVE DEVICE FOR A REVOLVING DOOR	ANDERSEN, HENRIK
08627750	5827856	250	04/02/1996	A METHOD OF TREATING INSULIN RESISTANCE	ANDERSEN, HENRIK S.
08625562	5716949	150	03/28/1996	NOVEL HETEROCYCLIC COMPOUNDS	ANDERSEN, HENRIK S.
08623807	5698551	150	03/29/1996	NOVEL HETEROCYCLIC COMPOUNDS	ANDERSEN, HENRIK S.
08544905	5712292	150	10/18/1995	N-SUBSTITUTED AZAHETEROCYCLIC CARBOXLIC ACIDS AND ESTERS THEREOF	ANDERSEN, HENRIK
08544682	5795888	150	10/18/1995	N-SUBSTITUTED AZAHETEROCYCLIC CARBOXYLIC ACIDS AND ESTERS THEREOF	ANDERSEN, HENRIK
08544500	5721254	150	10/18/1995	N-SUBSTITUTED AZAHETEROCYCLIC CARBOXYLIC ACIDS AND ESTERS THEREOF	ANDERSEN, HENRIK
08367648	5595989	150	01/03/1995	N-SUBSTITUTED AZAHETEROCYCLIC CARBOXYLIC ACIDS AND ESTERS THEREOF	ANDERSEN, HENRIK
08295750	Not Issued	161	09/01/1994	COLLECTION BAG FOR URINE FROM URINE-INCONTINENT AND UROSTOMY-OPERATED HUMANS AS WELL AS AN OUTLET VALVE SYSTEM THEREFOR	ANDERSEN, HENRIK B.
07595783	Not Issued	161	10/09/1990	PROCESS FOR PRODUCING HUMAN GROWTH HORMONE	ANDERSEN, HENRIK D.

07496129	Not Issued	161	03/19/1990	DNA SEQUENCE	ANDERSEN, HENRIK D.
07470396	5017493	150	01/25/1990	DNA SEQUENCE	ANDERSEN, HENRIK D.
07003380	Not Issued	166	01/20/1987	DNA SEQUENCE	ANDERSEN, HENRIK D.
07003379	Not Issued	166	01/20/1987	DNA SEQUENCE	ANDERSEN, HENRIK D.
06910230	Not Issued	166	10/02/1986	PROCESS FOR PRODUCING HUMAN GROWTH HORMONE	ANDERSEN, HENRIK D.
06887695	Not Issued	161	10/27/1986	DANISH CHECKERS	ANDERSEN, HENRIK S.
06374327	Not Issued	168	05/03/1982	METHOD FOR QUANTITATIVELY DETERMINING FAT IN A FAT-CONTAINING SAMPLE	ANDERSEN, HENRIK R.
06060411	4247773	150	07/25/1979	METHOD FOR QUANTITATIVELY DETERMINING FAT IN A FAT-CONTAINING SAMPLE	ANDERSEN, HENRIK R.

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Inventor Name Search Result

Your Search was:

Last Name = LICHTENBERG
First Name = JAKOB

Application#	Patent#	Status	Date Filed	Title	Inventor Name 3
60251862	Not Issued	159	12/08/2000	VIRTUAL TABULATION	LICHTENBERG, JAKOB
09996745	Not Issued	030	11/30/2001	METHOD OF CONFIGURING A PRODUCT	LICHTENBERG, JAKOB
09763753	Not Issued	041	04/27/2001	DATA STRUCTURE AND ITS USE	LICHTENBERG, JAKOB

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Continuity Information for 09/996745

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Claims Priority from Provisional Application 60251862

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